



ARUNAI ENGINEERING COLLEGE

TIRUVANNAMALAI

Department of Electrical & Electronics Engineering
International Journals

1. **Asst. Prof. C.R.Balamurugan**, S.P.Natarajan, M.Arumugam, “New Variable Amplitude Carrier Overlapping PWM methods for Three Phase Five Level Cascaded Inverter”, *Journal of Theoretical and Applied Information Technology*, Vol. 54, No.2, pp.294-303, August 2013.
2. **Asst. Prof. C.R.Balamurugan**, S.P. Natarajan, R.Bensraj, “Investigations on Various Bipolar PWM Strategies for Three Phase Five Level Cascaded Multilevel Inverter”, *NICE*, 2013
3. **Asst. Prof Arun.V.**, Shanthi, B. &Natarajan, S. P. “Comparative Study on New COPWM Techniques for Three Phase Cascaded Z-Source Inverter”*International Journal of Engineering Research & Technology (IJERT)*. Vol.2, 1–8 (2013).
4. **Asst. Prof Arun.V.**, Shanthi, B. & Natarajan, S. P. Performance Analysis of Single Phase Reduced Switch Asymmetric Multilevel Inverter. *International Journal of Advanced Research in Computer Science and Electronics Engineering (IJARCSEE)* Volume 2, Issue 6, Vol .2, 520–525 (2013).
5. **Asst. Prof Arun.V.**, Shanthi, B. & Natarajan, S. P. “Performance Analysis of Multicarrier SPWM Strategies for Three Phase Z - Source Seven Level Cascaded Inverter” *International Journal of Modern Engineering Research (IJMER)*. Vol.3, 204–211 (2013).
6. **Asst. Prof Arun.V** B.Shanthi, S. P. Natarajan. “Evaluation of New COPWM Techniques for Three Phase Seven Level Diode Clamped Z-Source Inverter” *Springer-Lecture Notes in Electrical Engineering*. Vol.188,136-146,(2013).

7. **Asst. Prof Arun.V** & Shanthi, B. “Performance Evaluation of Multicarrier SPWM Strategies for Three Phase Z - source Seven Level Diode Clamped Inverter” *International Journal of Engineering Trends and Technology (IJETT)*. Vol.4, 2217–2222 (2013).
8. **Asst. Prof Arun. V.**, Shanthi, B. & Natarajan, S. P. “ Unipolar PWM Control Technique having Inverted Sine Carrier for Trinary DC source Multilevel Inverter”, *International Journal of Advanced Scientific and Technical Research*. Vol.2, 448–460 (2013).
9. **Asst. Prof Arun.V.**, Shanthi, B. & Natarajan, S. P. “Investigation of Digital Control Strategy for Trinary DC Source Cascaded Multilevel Inverter” *International Journal of Emerging Trends in Engineering and Development*. Vol.1, 382–390 (2013).
10. **Asst. Prof Arun.V.**, Shanthi, B. & Natarajan, S. P. “Unipolar PWM Control Technique having Inverted Sine Carrier for an Asymmetric Reduced Switch Multilevel Inverter”, *International Journal of Engineering Science and Innovative Technology (IJESIT)*. Vol.2, 486–493 (2013).
11. **Asst. Prof R.Revathy**, S.P.Natarajan, **Asst. Prof C.R.Balamurugan**, “ Analysis of Control Strategies for Diode Clamped Multilevel Inverter”, *International Journal of Innovation and Applied Studies*, ISSN No.2028 - 9324, Vol. 3, No.1, pp.19-34, May 2013.
12. **Asst. Prof R.Revathy**, **Asst. Prof C.R.Balamurugan**, S.P.Natarajan, “Comparison Of Various Variable Amplitude Carrier Overlapping PWM Methods for Three Phase Five Level Cascaded Inverter”, *i-manager’s Journal on Digital Signal Processing*, Vol. 1 , No. 1, pp.18-28, January - March 2013.
13. **Asst. Prof R.Revathy**, **Asst. Prof C.R.Balamurugan**, S.P.Natarajan, “Design of New Multilevel Inverter Topology for various Unipolar PWM Strategies”, *International Conference*

in Magna on Emerging Engineering Trends, in Magna Engineering College, April 10th- 12th , 2013

Workshops

1. **Asst. Prof S.Ganesh kumar**, “Workshop on NPTEL”, Arunai College of Engineering, Tiruvannamalai ,13th July 2013.

2. **Asst. Prof R.Satheesh**, “Workshop on NPTEL”, Arunai College of Engineering, Tiruvannamalai, 13th July 2013.

3. **Asst. Prof N.Ezhilventhan**, “Economic load dispatch of quantum inspired particle swarm optimisation with value –point effect”, National conference, ARM engineering college Chennai at 5th April 2013.

4. **Asst. Prof K.Mohan**, “Workshop on NPTEL”, Arunai College of Engineering, Tiruvannamalai, 13th July 2013.

5. **Asst. Prof B. Janani**, “Workshop on MSP430 Ultra Low Power Microcontroller" in Anna University, Regional Centre, Coimbatore. “Workshop on Embedded Computing Systems" in Dr.N.G.P Institute of Technology, Coimbatore. “Workshop on CMOS Layout Design Flow Using Micro wind" in Anna University, Regional Centre, Coimbatore. “Workshop on MSP430 Particle Swarm Optimization And Its Variants: Concepts And Basic Implementation In MATLAB" in Anna University, Regional Centre, Coimbatore.

National/International Conferences

1. **Asst. Prof K.Arulkumar**, “Recent trends in matrix converters”- A technology review”, National conference ITPED 13, Velammal Engineering College, Chennai, March 2013.

2. **Asst. Prof S.Priya Dharshini**, “Comparative THD analysis of Diode clamped multi level Z-source inverter using PWM control techniques”, international conference at Arunai College of Engineering, Tiruvannamalai.

“Automated test equipment for sensor electronics”published in international systems(ICAES-2013) organised at CSIR-CEERI, Pilani.

3. **Asst. Prof D.Periyazhagar**, “Harmonic reduction using 18-pulse converter”, National conference GKM College of Engineering, Chennai, April 2013. “Harmonic souce and reduction techniques” International Conference at Crescent University Chennai, April 2013.

4. **Asst. Prof B.Janani**, “ALL-TERRAIN UNMANNED RECONNAISSANCE AUTOMATON (AURA)”, International Conference on “Competency Building Strategies In Business And Technology For Sustainable Development” at Sri Ganesh School Of Business Management. “INDUSTRIAL INFORMATICS PROJECTS AND LABORATORIES USING SMARTPHONE”, National Conference on “Emerging Trends In Electrical, Electronics And Control Engineering” at University College Of Engineering – Panruti. “INDUSTRIAL INFORMATICS PROJECTS AND LABORATORIES USING SMARTPHONE” , National Conference on “Emerging Scenario in Electrical and Electronics Engineering” at Mailam Engineering College.